

A Vaste Separation

Chip C

Chip B

Chip A

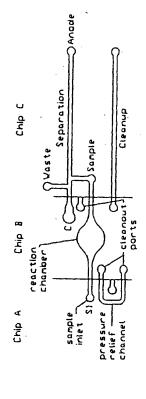
reaction chamber]

sample inter

rellef channel

LEGEND

FIGURE 1

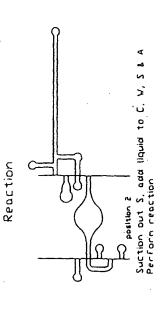


LEGEND

Pump notrix into A, suction out E, W, S  $\mbox{\bf L}$  Add sample to SI port

260

Preparation



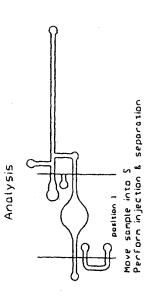
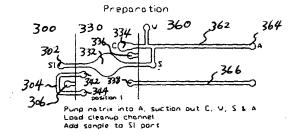
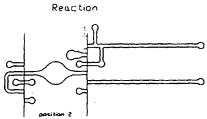
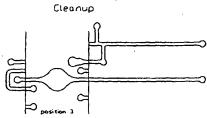


FIGURE 2

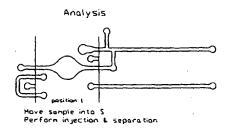


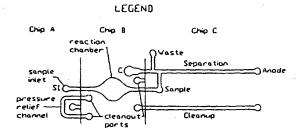


Suction out S, add liquid to C, V, S & A Perform reaction



Move sample into cleanup channel Elute clean sample out of cleanup channel





## FIGURE 3

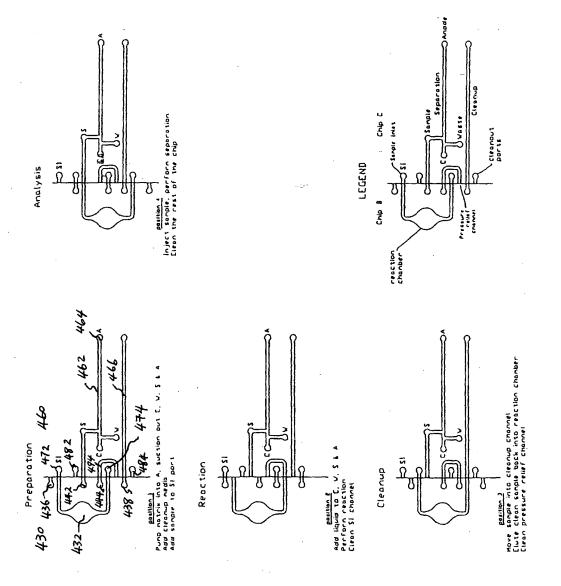


FIGURE 4

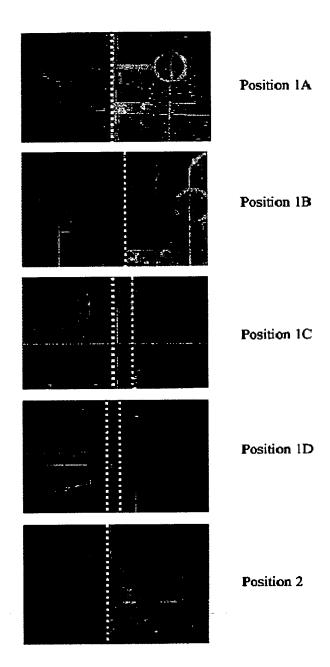
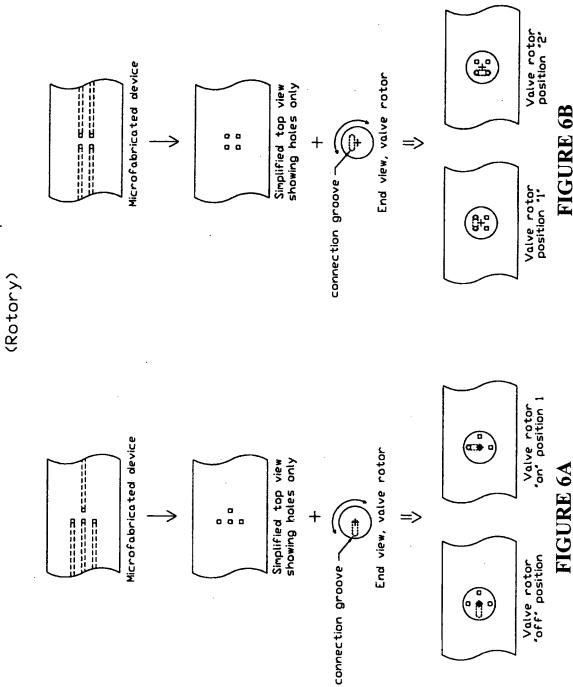
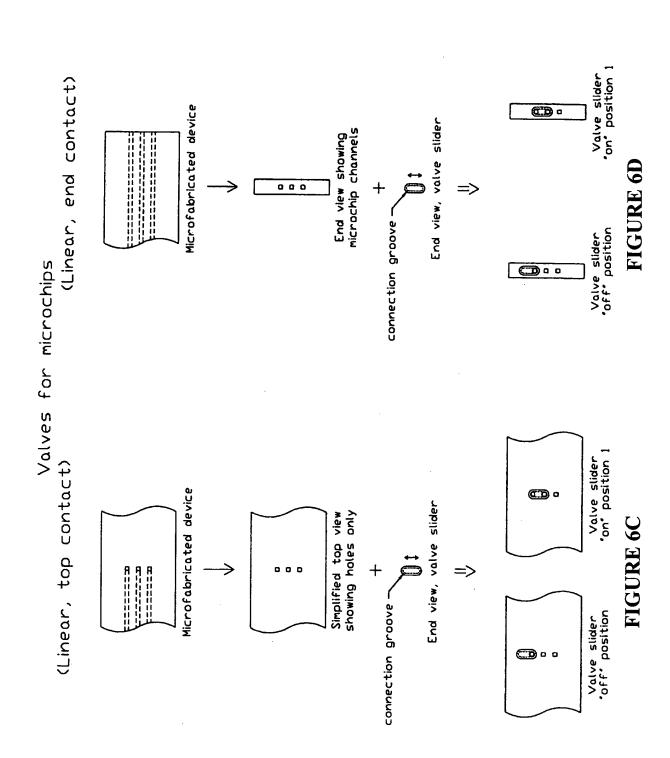


FIGURE 5



Valves for microchips

FIGURE 6B



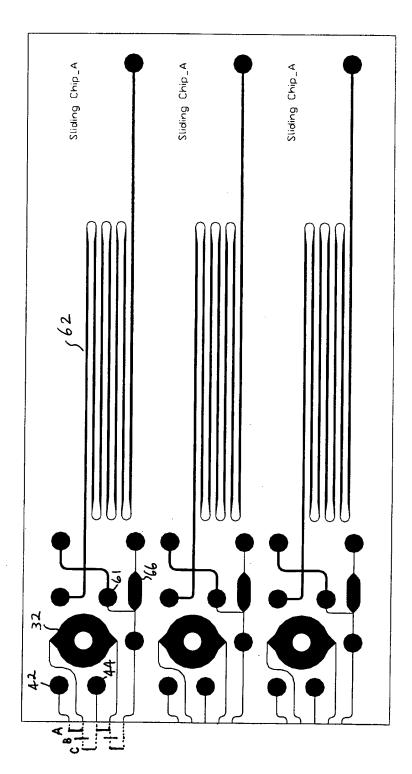


FIGURE 7A

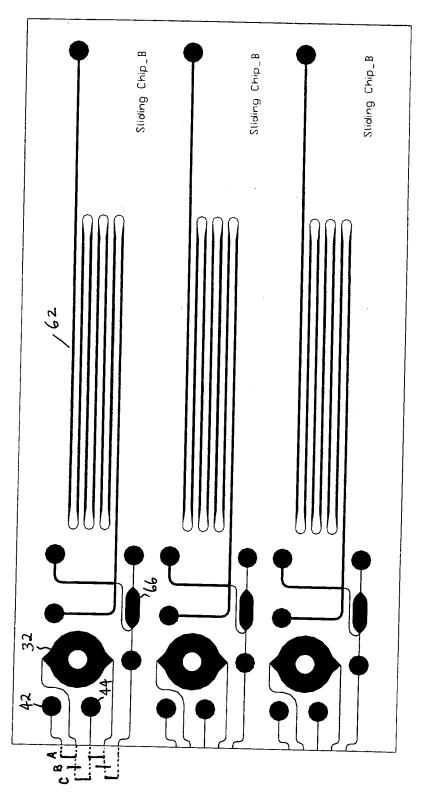


FIGURE 7B

Fabricating very small, high aspect ratio holes in glass

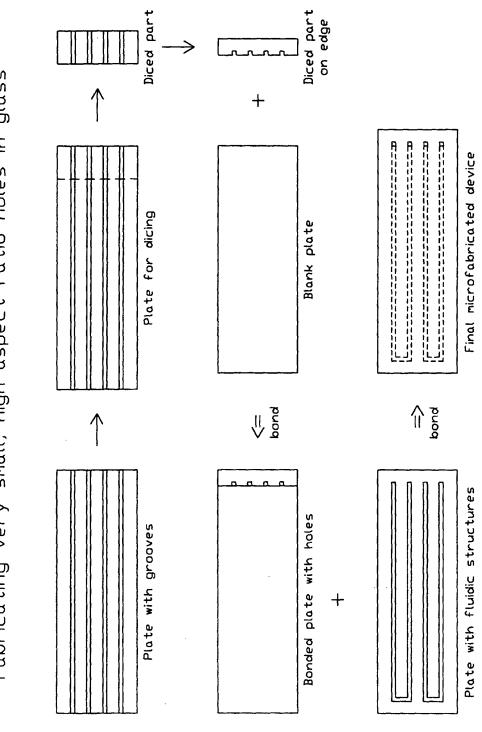


FIGURE 8